

Amendments to the Abstract:

Please replace the Abstract of the Disclosure which begins on page 25, with the following amended Abstract:

According to the present invention, a three-line bus is used for communication between a first digital unit (1) and two other digital units (3). The three-line bus includes a system clock line (SCL) as well as a data transmission line (SD) through which data is sent from the two other units (3), i.e. transmitting units, to the first unit (1), i.e. receiving unit. The system uses an authorization line (WS) for determining which of the two transmitting units (3) is capable of writing data on the data transmission line (SD) and when it can do so. The communication ~~form~~ from the first unit (1), now operating as a transmitter, to the two other units (3), now operating as receivers, is made possible by the fact that data signals are also injected in the authorization line (WS) and transmitted through ~~said~~ the line together with the authorization control signals.